

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

KARSTEN WIECZOREK
THOMAS FEUDEL
THORSTEN KAMMLER
WOLFGANG BUCHHOLTZ

Serial No.: 10/790,852

Filed: March 2, 2004

For: FIELD EFFECT TRANSISTOR
HAVING A DOPED GATE
ELECTRODE WITH REDUCED GATE
DEPLETION AND METHOD OF
FORMING THE TRANSISTOR

Confirmation No.: 3105

Group Art Unit: 2815

Examiner: N. Drew Richards

Attorney Docket: 2000.110600/DE0347

Customer No.: 23720

SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 be considered by the Examiner and made of record. A copy of the listed foreign official action has been filed concurrently herewith.

In accordance with 37 C.F.R. §§ 1.97(g),(h), this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be construed to be an admission that the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

In accordance with 37 C.F.R § 1.97(c)(1), Applicants hereby certify that each item of information contained in this Supplemental Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the present statement, as evidenced by the date of the enclosed official action from the European Patent Office.

Applicants respectfully request that the listed documents be made of record in the present case.

Respectfully submitted,

WILLIAMS, MORGAN & AMERSON
CUSTOMER NO. 23720

Date: February 11, 2008

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